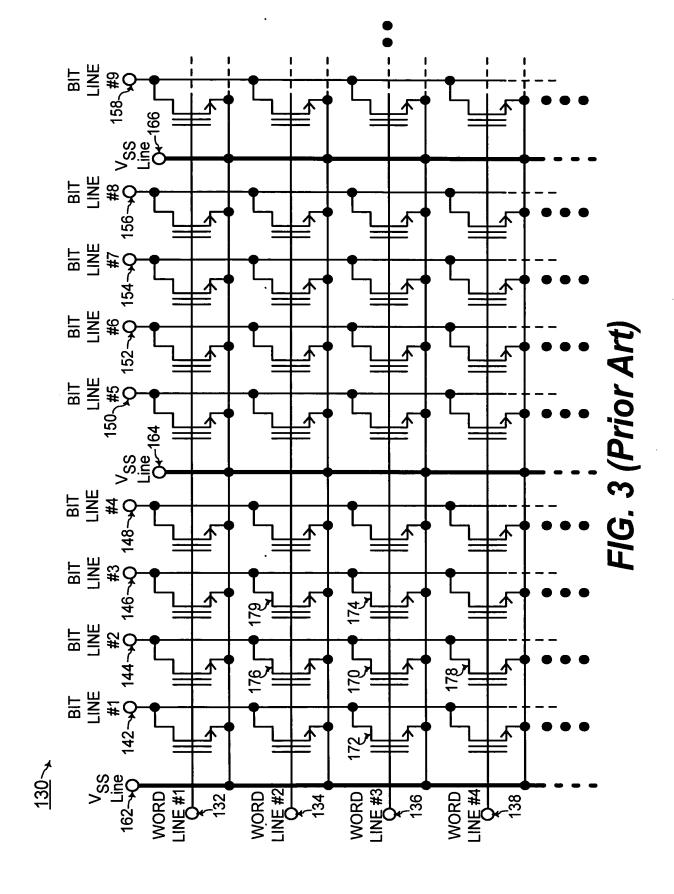
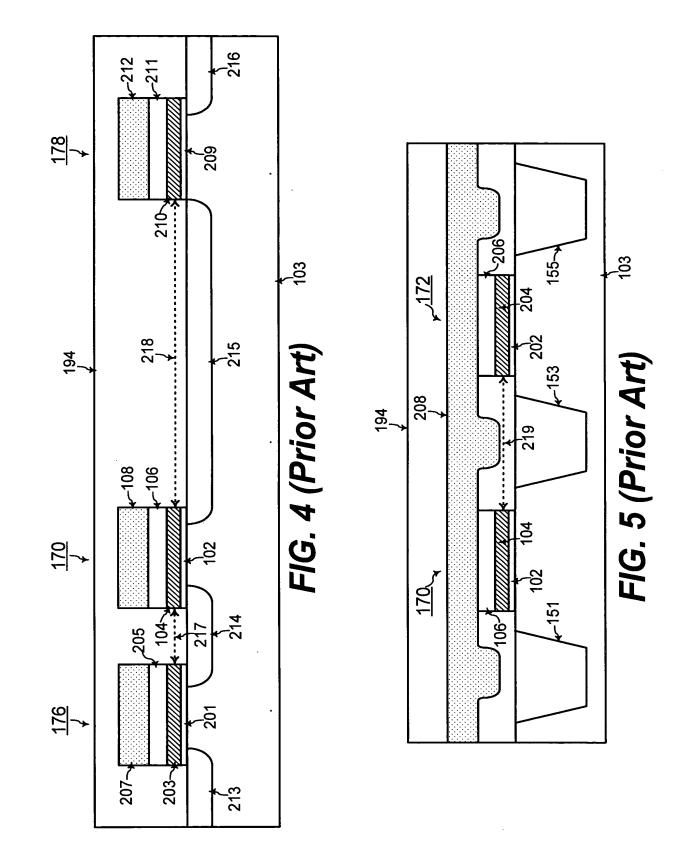


FIG. 2 (Prior Art)



Title: Minimization of FG-FG Coupling in Flash Memory Inventor(s): Richard Fastow and Sheunghee Park Case No.: 03-03 (H1970), Sheet 3/8



Title: Minimization of FG-FG Coupling in Flash Memory Inventor(s): Richard Fastow and Sheunghee Park Case No.: 03-03 (H1970), Sheet 4/8

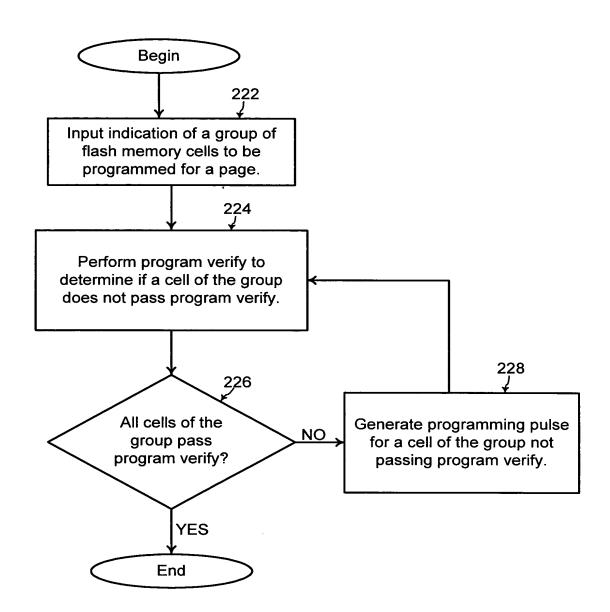
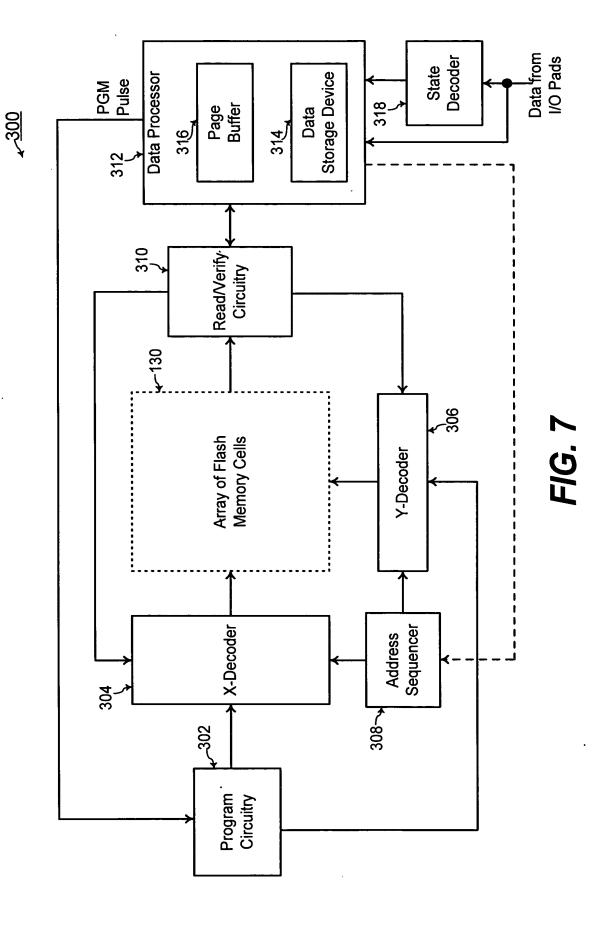


FIG. 6 (Prior Art)

Title: System and Method for Minimizing Inter-Cell Charge Coupling in Flash Memory Inventor(s): Richard Fastow and Sheunghee Park Case No.: 03-03 (H1970), Sheet 5/8



Title: Minimization of FG-FG Coupling in Flash Memory Inventor(s): Richard Fastow and Sheunghee Park Case No.: 03-03 (H1970), Sheet 6/8

